

Form PTO-1449



DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
M122-356

SERIAL NO. 3/20/91

LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

APPLICANT
Brent Keeth et al.

FILING DATE

GROUP

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AL						
	AM						
	AN						
	AO						
	AP						

OTHER PRIOR ART (including Author, Title, Date, Pertinent Pages, Etc.)

	AR	Sugiyasu, T. et al., "A 30ns 256-Mb DRAM With A Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28, No. 11, November 1993, pp. 1092-1097
	AS	Kinukawa et al., "256-Mb DRAM Circuit Technologies for File Applications", IEEE Journal of Solid-State Circuits, Vol. 28, No. 11, November 1993, pp. 1105-1113
	AT	
EXAMINER		
DATE CONSIDERED		